



PTO/SB/08B (10-01)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 2 of 2

<i>Complete if Known</i>	
Application Number	09 / 521,280
Filing Date	07-Mar-2000
First Named Inventor	Stephan Voges
Group Art Unit	2122
Examiner Name	Eric B. Kiss
Attorney Docket Number	EFIM0252

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
EBK		Arnold, J. M., "An Architecture Simulator For National Semiconductor's Adaptive Processing	
		Architecture (NAPA)," FPGAS For Custom Computing Machines, IEEE Comput. Soc., pp 271-72,	
		April 15, 1998.	
EBK		Parson D. et al., "A Framework For Simulating Heterogeneous Virtual Processors," Simulation	
		Symposium, IEEE Comput. Soc., pp. 58-67, April 11, 1999.	

Examiner
Signature

Eric D. K.

Date
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7/16/2003

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of

Complete if Known

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Examiner Name	Eric B. Kiss
Attorney Docket Number	EFIM0252

U.S. PATENT DOCUMENTS

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